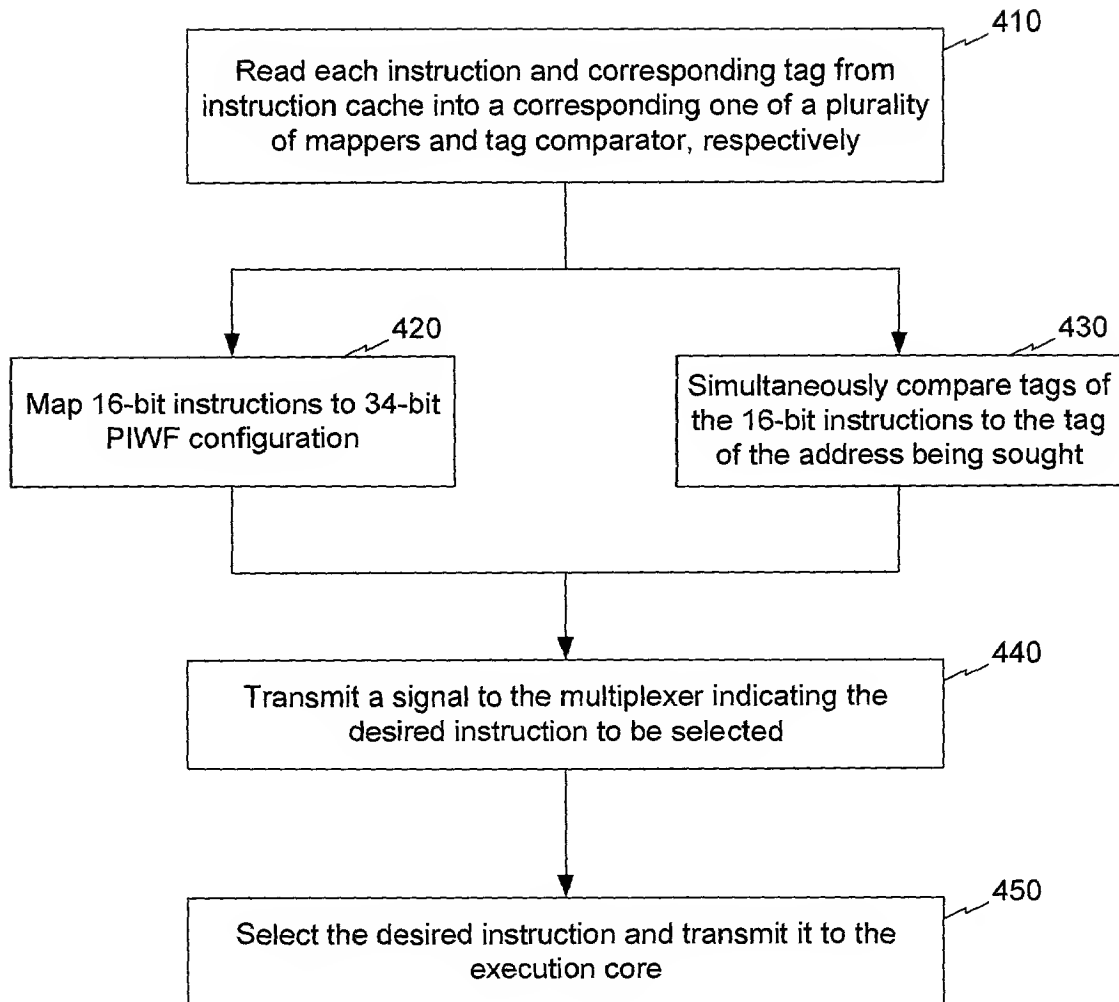


FIG. 3



**FIG. 4**

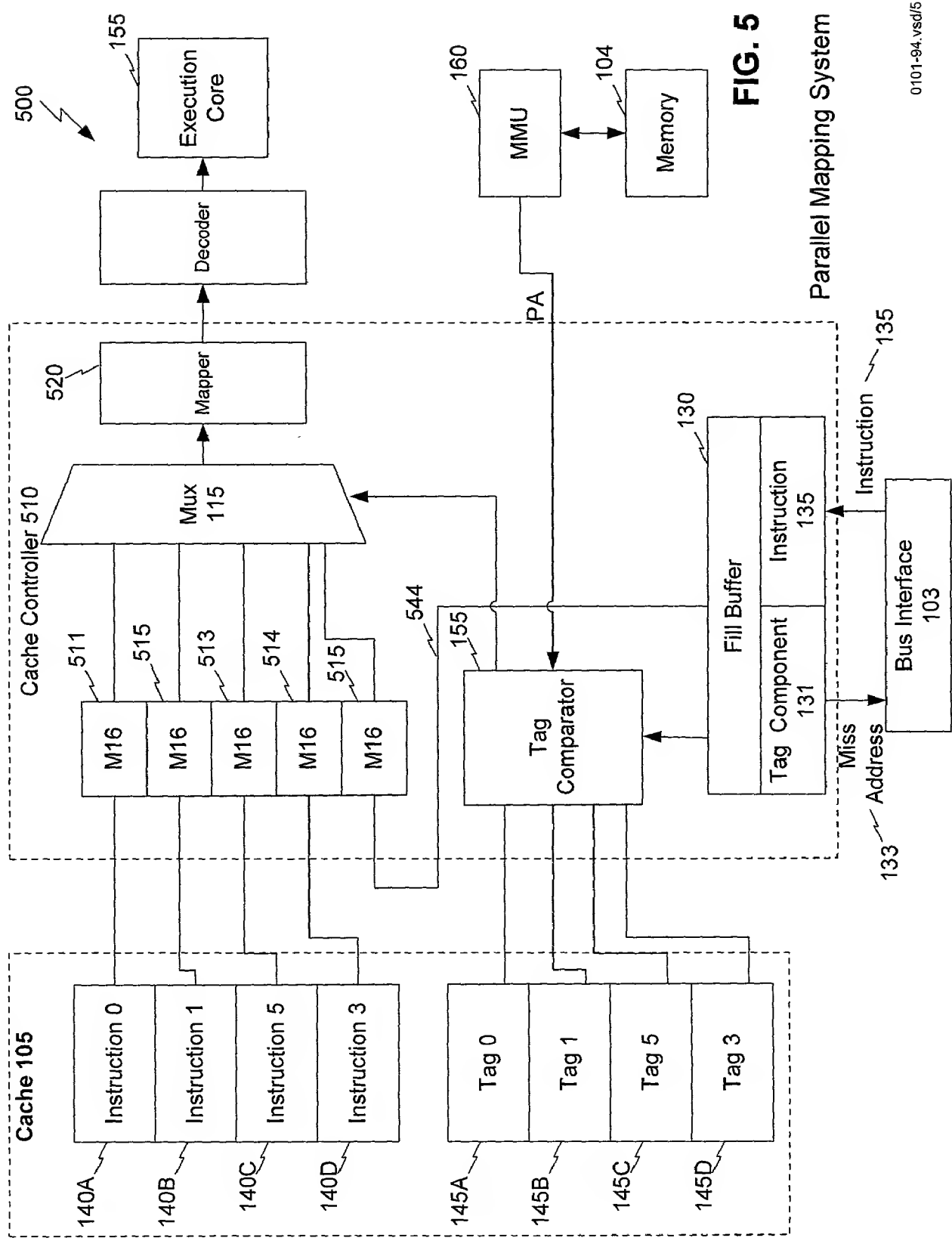


FIG. 5

Parallel Mapping System